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09/442,727	11/18/1999	SADAHARU SATO	450100-02171	6321

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EXAMINER

VAUGHAN, MICHAEL R

ART UNIT PAPER NUMBER

2131

DATE MAILED: 09/11/2003

Please find below and/or attached an Office communication concerning this application or proceeding.

**Office Action Summary**

Application No.

09/442,727

Applicant(s)

SATO, SADAHARU

Examiner

Michael R Vaughan

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

**Period for Reply**

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133).
- Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

**Status**

- 1) ☐ Responsive to communication(s) filed on \_\_\_\_.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

**Disposition of Claims**

- 4) ☒ Claim(s) 1-8 is/are pending in the application.
- 4a) Of the above claim(s) \_\_\_\_ is/are withdrawn from consideration.
- 5) ☐ Claim(s) \_\_\_\_ is/are allowed.
- 6) ☒ Claim(s) 1-8 is/are rejected.
- 7) ☐ Claim(s) \_\_\_\_ is/are objected to.
- 8) ☐ Claim(s) \_\_\_\_ are subject to restriction and/or election requirement.

**Application Papers**

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 18 November 1999 is/are: a) ☒ accepted or b) ☐ objected to by the Examiner.
- Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
- 11) ☐ The proposed drawing correction filed on \_\_\_\_ is: a) ☐ approved b) ☐ disapproved by the Examiner.
- If approved, corrected drawings are required in reply to this Office action.
- 12) ☐ The oath or declaration is objected to by the Examiner.

**Priority under 35 U.S.C. §§ 119 and 120**

- 13) ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☒ All b) ☐ Some \* c) ☐ None of:
1. ☒ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- \* See the attached detailed Office action for a list of the certified copies not received.
- 14) ☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. § 119(e) (to a provisional application).
- a) ☐ The translation of the foreign language provisional application has been received.
- 15) ☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. §§ 120 and/or 121.

**Attachment(s)**

- 1) ☒ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☐ Information Disclosure Statement(s) (PTO-1449) Paper No(s) \_\_\_\_.
- 4) ☐ Interview Summary (PTO-413) Paper No(s). \_\_\_\_.
- 5) ☐ Notice of Informal Patent Application (PTO-152)
- 6) ☐ Other: \_\_\_\_\_.

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### Detailed Action

Claims 1-8 have been examined and are pending.

### ***Claim Rejections - 35 USC § 102***

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

Claims 1-8 are rejected under 35 U.S.C. 102(b) as being anticipated by Szczutkowski et al. (USP 4,817,146).

As per claim 1, Szczutkowski et al. teach:

A cipher processing circuit for enciphering data (column 4, lines 42-66);

A transmission circuit for adding the enciphering information to the data enciphered in the cipher processing circuit (see FIG. 1);

Transmitting the result to the serial interface bus (column 8, lines 60-63);

Confirming the continuity of the cipher mode (column 7, lines 5-40 and column 8, lines 20-40);

Transmitting in a different cipher mode when a discontinuity is confirmed (column 20, lines 16-36).

As per claim 2, Szczutkowski et al. teach setting the enciphering information in a predetermined region of a header of the packet (column 17, lines 10-12 and column 19, lines 34-51).

As per claim 3, Szczutkowski et al. teach:

A holding means in which information of at least one cipher mode is set (FIG. 1);

A control means for specifying a mode to encipher (FIG. 1);

A cipher processing circuit including a cipher mode selection circuit and a cipher engine circuit for enciphering data and outputting data (FIG. 1 and column 7, lines 5-45);

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A transmission circuit for adding the enciphering information to the enciphered data (FIG. 1);  
Transmitting the result to the serial interface bus (column 8, lines 60-63);  
Confirming the continuity of the cipher mode (column 7, lines 5-40 and column 8, lines 20-40);  
Transmitting in a different cipher mode when a discontinuity is confirmed (column 20, lines 16-36).

As per claim 4, Szczutkowski et al. teach setting the enciphering information in a predetermined region of a header of the packet (column 17, lines 10-12 and column 19, lines 34-51).

As per claim 5, Szczutkowski et al. teach:

A storing means (FIG. 1);

A holding means in which information of at least one cipher mode is set (FIG. 1);

A control means for specifying a mode to encipher (FIG. 1);

A cipher processing circuit including a cipher mode selection circuit for selecting cipher mode information specified by the control means from the holding means and a cipher engine (DES) circuit for enciphering the data to be transmitted in the cipher mode selected and outputting the enciphered data (FIG. 1 and column 7, lines 5-45);

A first transmission circuit for generating time information (column 17, lines 3-47) to output received data on a receiving side to an application side (column 11, lines 43-48);

A second transmission circuit for reading enciphered data (FIG. 1), generating packet data (FIG. 1), setting enciphering information in a packet header (column 17, lines 10-12 and column 19, lines 34-51) and transmitting the result to a serial interface bus (column 8, lines 60-63), confirming the continuity of the cipher mode (column 7, lines 5-40 and column 8, lines 20-40), and transmitting in a different cipher mode when a discontinuity is confirmed (column 20, lines 16-36).

As per claim 6, Szczutkowski et al. teach:

A cipher processing circuit for enciphering data to be transmitted by a predetermined cipher mode (column 7, lines 5-45) at the time of transmission and deciphering the received enciphered data based on

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the enciphering information included in the received packet data (column 19, line 34 – column 20, line 36);

A transmission circuit for adding enciphering information to the enciphered data (column 19, lines 34-51 and column 17, lines 10-12), transmitting result to a serial interface bus (column 8, lines 60-63), confirming the continuity of the cipher mode (column 7, lines 5-40 and column 8, lines 20-40), and transmitting in a different cipher mode when a discontinuity is confirmed (column 20, lines 16-36).

As per claim 7, Szczutkowski et al. teach setting the enciphering information in a predetermined region of a header of the packet (column 17, lines 10-12 and column 19, lines 34-51).

As per claim 8, Szczutkowski et al. teach:

A first storing means (FIG. 6);

A second storing means (FIG. 6);

A holding means in which information of at least one cipher mode is set (FIG. 1);

A control means for specifying a mode to encipher (FIG. 1);

A first reception circuit for storing time information, enciphered data, and the enciphering information from received packets (FIG. 1, 2, 6, and column 20, lines 36-40);

A second reception circuit for outputting enciphering information and enciphered data to an application based on time information (FIG. 1, 2, 6, and column 21, lines 58-63 and column 17, lines 5-48);

A cipher processing circuit including a cipher mode detection circuit (column 20, lines 16-36);

A cipher mode selection circuit (column 7, lines 31-45);

A cipher engine for enciphering and deciphering (FIG. 1);

A first transmission circuit for generating time information (column 17, lines 3-47) to output received data on a receiving side to an application side (column 11, lines 43-48);

A second transmission circuit for reading enciphered data (FIG. 1), generating packet data (FIG. 1), setting enciphering information in a packet header (column 17, lines 10-12 and column 19, lines 34-

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51) and transmitting the result to a serial interface bus (column 8, lines 60-63), confirming the continuity of the cipher mode (column 7, lines 5-40 and column 8, lines 20-40), and transmitting in a different cipher mode when a discontinuity is confirmed (column 20, lines 16-36).

### **Conclusion**

The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

Kaufman et al, U.S. Patent 5,081,678

Szczutkowski et al, U.S. Patent 4,757,536

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Michael R Vaughan whose telephone number is 703-305-0354. The examiner can normally be reached on M-F 7-3:30.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Ayaz Sheikh can be reached on 703-305-9648. The fax phone number for the organization where this application or proceeding is assigned is (703) 872-9306.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is 703-305-3900.

MV  
Michael R Vaughan  
Examiner  
Art Unit 2131

  
AYAZ SHEIKH  
SUPERVISORY PATENT EXAMINER  
TECHNOLOGY CENTER 2100